

<b>Notice of References Cited</b>	Application/Control No. 09/370,508	Applicant(s)/Patent Under Reexamination SHARMA ET AL.	
	Examiner DuyVu n Deo	Art Unit 1765	Page 1 of 1

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**NON-PATENT DOCUMENTS**

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	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Vol. 1, pg 539-542
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
 Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.